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Jon W. Hallman
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05/03/2005 FIGEBREH2 00000033 502257 10619711

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Tina Kavanagh

(Depositor's name)

Tina Kavanagh

(Signature)

May 2, 2005

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/619,711	07/14/2003	Jason Cheng	M-9559-2P US	7093

TITLE OF INVENTION: PROGRAMMABLE LOGIC DEVICE WITH ENHANCED WIDE INPUT PRODUCT TERM CASCADING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$0	\$1400	05/16/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
CHO, JAMES HYONCHOL	2819	326-041000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).	2. For printing on the patent front page, list (1) the names of up to 3 registered patent attorneys or agents OR, alternatively, (2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.
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(A) NAME OF ASSIGNEE**(B) RESIDENCE: (CITY and STATE OR COUNTRY)**

Lattice Semiconductor Corporation

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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- b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date May 2, 2005

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Chan-Chi Jason Cheng; Cyrus Tsui; Satwant Singh; Albert Chan; Ju
 Shen; Clement Lee
 Assignee: Lattice Semiconductor Corporation
 Title: Programmable Logic Device With Enhanced Wide Input Product Term
 Cascading
 Serial No.: 10/619,711 Filing Date: 7/14/2003
 Examiner: Cho, James Hyonchol Group Art Unit: 2819
 Docket No.: M-9559-2P US Confirmation No. 7093

Irvine, California
May 2, 2005

Fax No.: 1-703-746-4000

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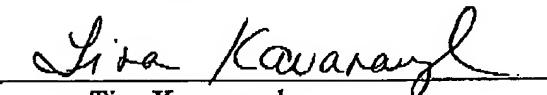
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1. Issue Fee Transmittal (1 page)
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Dated: May 2, 2005


Tina Kavanaugh

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